

EXPRESS MAIL CERTIFICATE

I hereby certify that the above paper/fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated below and is addressed to the Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 15, 2002, Express Mail No. EV298900430US. Signature of Person mailing paper/fee:

Grace A. Vaca

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: October 15, 2003

Marc Francis WILDING and Daniel Alan WOOD

Serial No.: To Be Assigned

Group Art Unit: To Be Assigned

Filed: Herewith

Examiner: To Be Assigned

For: HEAP AND STACK LAYOUT FOR MULTITHREADED PROCESS IN A
PROCESSING SYSTEM

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE
STATEMENT UNDER 37 C.F.R. §1.97

Sir:

Pursuant to 37 C.F.R. §1.97 and §1.98, Applicants submit for consideration in the above-identified application the documents listed on the attached Form PTO-1449. The Examiner is requested to make these documents of record. This Information Disclosure Statement is being submitted:

- ☐ With copies.
- ☐ Without copies. Copies of the documents were previously submitted in an Information Disclosure Statement and/or Office Action, directed to the related application Serial No. **, filed **. This protocol conforms with 37 C.F.R. §1.98(d) and M.P.E.P. 609 (A)(2).
- This Information Disclosure Statement is being submitted with only copies of non-U.S. patent publication(s) and non-patent literature. This protocol conforms with 37 CFR 1.98(a)(2)(i), which waives the requirement for submitting a copy of each cited U.S. Patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003.

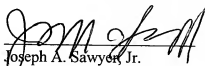
ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /KT/

- ☒ With the application; accordingly, no fee or separate requirements are required.
- ☐ After receipt of a first Office Action on the merits, but before the mailing date of a Final Office Action under §1.113, or a Notice of Allowance §1.311. Accompanied by one of:
- ☐ Certification under 37 CFR §1.97(e); or
 - ☐ The fee set forth in §1.17(p)
- ☐ After mailing of a final Office Action or Notice of Allowance, but on or before payment of the issue fee. Accompanied by:
- ☐ Certification under 37 CFR §1.97(e); and
 - ☐ The fee set forth in §1.17(p)
- ☐ A Certification under 37 C.F.R. §1.97(e) is provided below:
- ☐ I hereby certify that each item of information was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.
- or-
- ☐ I hereby certify that no item of information was cited in a communication from a foreign patent office in a counterpart foreign application or, to the best of my knowledge after making a reasonable inquiry, was known to any individual designated in 37 C.F.R. §1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

If any unresolved issues remain, please contact Applicant's attorney at the telephone number indicated below. The Commissioner is hereby authorized to charge any fees associated with this communication, or credit any overpayment, to Deposit Account No. 09-0460 (IBM Corporation).

Respectfully submitted,
SAWYER LAW GROUP LLP


Joseph A. Sawyer, Jr.
Attorney for Applicant(s)
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(650) 493-4540

October 15, 2003
Date

FORM PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. SVL920030066US1/2890P	SERIAL NO. To Be Assigned
	APPLICANT: Mark Francis WILDING and Daniel Alan WOOD	
	FILING DATE: Herewith	GROUP: To Be Assigned

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	US 2001/0000821	05/02/01	Kolodner et al.	711	170	
	US 2001/0025295	09/27/01	Kawachiya et al.	709	106	
	US 2002/0095453	07/18/02	Steensgaard	709	107	
	US 2002/0104077	08/01/02	Charnell et al.	717	162	
	US 2002/0199069	12/26/02	Joseph	711	147	
	US 2003/0018826	01/23/03	Chaudhry et al.	709	310	
	US 2002/0129306	09/12/02	Flanagan et al.	714	100	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

Date unknown		Hammersley, S.D. et al., "Sharing a Single Stack Among Multiple Concurrent Threads," IBM Technical Disclosure Bulletin, Vol. 36, No. 10, October 1993, pp. 235-237.
KT		
Date unknown		Carlson, W.C. et al., "Thread Stack Monitor for Multithreaded Applications," IBM Technical Disclosure Bulletin, Vol. 39, No. 3, March 1996, pp. 283-284.
KT		
Date unknown		Henderson, Fergus, "Accurate Garbage Collection in an Uncooperative Environment," Dept. of Computer Science & Software Engineering, The University of Melbourne, Victoria, Australia, 7 pages.

EXAMINER

/Kenneth Tang/

DATE CONSIDERED

06/24/2008

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /KT/

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EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	6 4 1 2 0 5 3	06/25/02	Bonola	711	170	
	6 4 2 7 1 9 5	07/30/02	McGowen et al.	711	153	
	6 4 3 3 5 8 6	08/13/02	Ooishi	326	93	
	6 4 9 9 0 9 4	12/24/02	Fraser et al.	711	171	
	6 5 0 5 2 7 5	01/07/03	Weissman et al.	711	154	
	6 5 0 7 9 0 3	01/14/03	Beatty, III et al.	711	173	

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

Date unknown KT		Kuechlin, Wolfgang W. et al., "On Multi-Threaded List-Processing and Garbage Collection," IEEE, 1991, pp. 894-897.
		"Staggering Call Stack Offsets for Multiple Duplicate Control Threads," IBM Docket ROC920020014US1, 9 pages.
		Conference on Programming Language Design and Implementation Proceedings of the 1997 ACM SIGPLAN Conference on Programming Language Design and Implementation, 1997, Las Vegas, Nevada, 5 pages.

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